	81FE								
	Substitute	form 1449/F	PTO		Application Number	10/511,688			
DE	C 1 4 2007				U.S. Filing Date:	June 6, 2005			
13	ln/	formatio	n Disclosu By Applica neets as necessa	re	First Named Inventor:	Michael BALDISCHWEILER			
(A)	65ta	atement	By Applica	nt	Art Unit:	2183			
~	CEMARS (US	se as many st	neets as necessa	ry)	Examiner Name:	Not Assigned			
					Attorney Docket Number:	WACHP002			
	Sheet	1	of	1					

## **U.S. Patent Documents**

Examiner Initial	Cite No.	Document No.	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
	Α	5,903,759	5/1999	Sun et al.	
	В	6,263,429	7/2001	Siska	
	С				
	D				
	Ε				
	F				
	G				
	Н				
	1				
	J				
	K				

**Foreign Patent Documents** 

Totolgh Faterit Dodaments							
Examiner Initial	Cite No.	Document No.	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear	Transl YES	ation NO
	L	FR 2 783 065	3/2000	Olivier			x
	М	FR 2 785 695	5/2000	Schultz et al.			х
	N						
	0					,	Ì
	Р		1				1
-	Q				<b>†</b>	<u> </u>	1

## **Non Patent Literature Documents**

Examiner	Cite	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book,
1		magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or
Initial	No.	country where published.
	R	Michael J. Zastre, "Compacting Object Code via Parameterized Procedural Abstraction,"
		Thesis, University of Victoria, 1993 (XP-001199580)
	S	Christian Ferdinand, "Post Pass Code Compaction at the Assembly Level for C16x,
		Infineon Technologies, pp. 35-36 (article retrieved from the Internet in October 2001 at
		URL: www.absint.com/aipop/aiPop_c3935.pdf)
	Т	Saumya K. Debray et al., "Compiler Techniques for Code Compaction," ACM
		Transactions on Programming Languages and Systems, Vol. 22, No. 2, March 2000, pp.
		378-415 (pages numbered 1-38 on attached copy)
	U	Website content entitled "Raisonance Announces New Post Linker Optimizer for 80C51
		Microcontroller Architecture," retrieved from the Internet in October 2001 (2 pages)
		(URL: http://www.8052.com/news.phtml?NEWSID=8)
	V	Christian Ferdinand, "Liebling, ich habe den Code geschrumpft," Design & Elektronik
		No. 1, 2001, pp. 113-115
	W	W. Rankle and W. Effing, "Smart Card Handbook," 3d Ed., 2003, John Wiley & Sons,
		Ltd., pp. 62-91

Francisco Olamatica	Data Assertational	
Examiner Signature	Date Considered	
=xammor orginatare	Date College Ca	

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.